Abstract of the Disclosure

A mechanism is disclosed for adjusting one or more operational parameters of a component in such a way that the adjustment has minimal impact on graphics display. When a component (e.g. a processor) wishes to adjust one or more of its operational parameters (e.g. operating clock frequency), it submits a request to a graphics processing mechanism (GPM). In response, the GPM, which controls multiple displays, sends a response to the component at a proper time to cause the component to adjust the operational parameter during a time period in which a first display and a second display are both experiencing a blank period. By coordinating the timing of the adjustment in this way, it is possible to keep the size of a video memory to a minimum.